

#5/Amct A
4-17-02 JLD

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Colleen J. Dew

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit 2822

<u>In re application of</u>	:	April 15, 2002
Axel Brintzinger et al.	:	Examiner: Monica Lewis
Serial No. : 09/873,537	:	
Filed: June 4, 2001	:	IBM Corporation
	:	Dept. 18G/Bldg, 300-482
Title: DUAL DAMASCENE ANTI-FUSE	:	2070 Route 52
WITH VIA BEFORE WIRE	:	Hopewell Junction, NY
	:	12533-6531

Amendment

Commissioner for Patents and Trademarks
Washington, D.C. 20231

Sir:

Kindly amend the claims as follows:

- ALC
- 1 22. (amended) An interconnect structure in which an anti-fuse dielectric is
 - 2 formed therein comprising:
 - 3 a substrate having a first level of electrically conductive features;
 - 4 a patterned anti-fuse dielectric layer formed on said substrate, wherein
 - 5 said patterned anti-fuse dielectric layer includes an opening to at least one
 - 6 of said first level of electrically conductive features;